



ON Semiconductor®

**ON Semiconductor**  
**DATA SHEET****2SK3486** — N-Channel Silicon MOSFET  
**General-Purpose Switching Device**  
**Applications****Features**

- Low ON-resistance.
- Ultrahigh-speed switching.
- 2.5V drive.

**Specifications****Absolute Maximum Ratings** at  $T_a=25^\circ\text{C}$ 

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	$V_{DS}$		20	V
Gate-to-Source Voltage	$V_{GS}$		$\pm 10$	V
Drain Current (DC)	$I_D$		8	A
Drain Current (Pulse)	$I_{DP}$	$PW \leq 10\mu\text{s}$ , duty cycle $\leq 1\%$	32	A
Allowable Power Dissipation	$P_D$	Mounted on a ceramic board (250mm <sup>2</sup> ×0.8mm)	1.5	W
		$T_c=25^\circ\text{C}$	3.5	W
Channel Temperature	$T_{ch}$		150	$^\circ\text{C}$
Storage Temperature	$T_{stg}$		-55 to +150	$^\circ\text{C}$

**Electrical Characteristics** at  $T_a=25^\circ\text{C}$ 

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D=1\text{mA}$ , $V_{GS}=0\text{V}$	20			V
Zero-Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=20\text{V}$ , $V_{GS}=0\text{V}$			1	$\mu\text{A}$
Gate-to-Source Leakage Current	$I_{GSS}$	$V_{GS}=\pm 8\text{V}$ , $V_{DS}=0\text{V}$			$\pm 10$	$\mu\text{A}$
Cutoff Voltage	$V_{GS(off)}$	$V_{DS}=10\text{V}$ , $I_D=1\text{mA}$	0.4		1.3	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS}=10\text{V}$ , $I_D=4\text{A}$	7	10		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D=4\text{A}$ , $V_{GS}=4\text{V}$		33	43	$\text{m}\Omega$
	$R_{DS(on)2}$	$I_D=2\text{A}$ , $V_{GS}=2.5\text{V}$		40	56	$\text{m}\Omega$

Marking : LC

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# 2SK3486

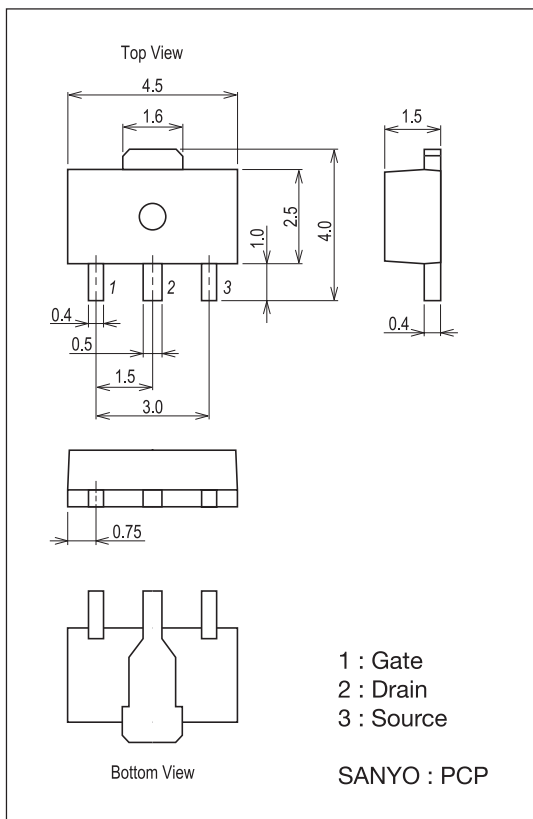
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Input Capacitance	Ciss	$V_{DS}=10V, f=1MHz$		570		pF
Output Capacitance	Coss	$V_{DS}=10V, f=1MHz$		110		pF
Reverse Transfer Capacitance	Crss	$V_{DS}=10V, f=1MHz$		80		pF
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit.		13		ns
Rise Time	$t_r$	See specified Test Circuit.		28		ns
Turn-OFF Delay Time	$t_{d(off)}$	See specified Test Circuit.		53		ns
Fall Time	$t_f$	See specified Test Circuit.		65		ns
Total Gate Charge	Qg	$V_{DS}=10V, V_{GS}=4V, I_D=8A$		7.6		nC
Gate-to-Source Charge	Qgs	$V_{DS}=10V, V_{GS}=4V, I_D=8A$		1.2		nC
Gate-to-Drain "Miller" Charge	Qgd	$V_{DS}=10V, V_{GS}=4V, I_D=8A$		2.1		nC
Diode Forward Voltage	VSD	$I_S=8A, V_{GS}=0V$		0.9	1.2	V

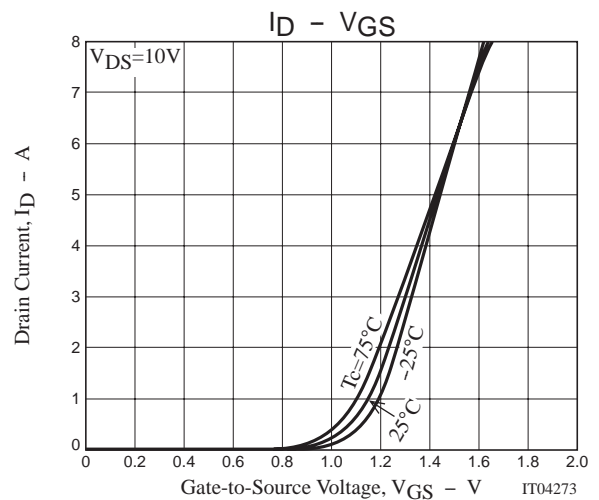
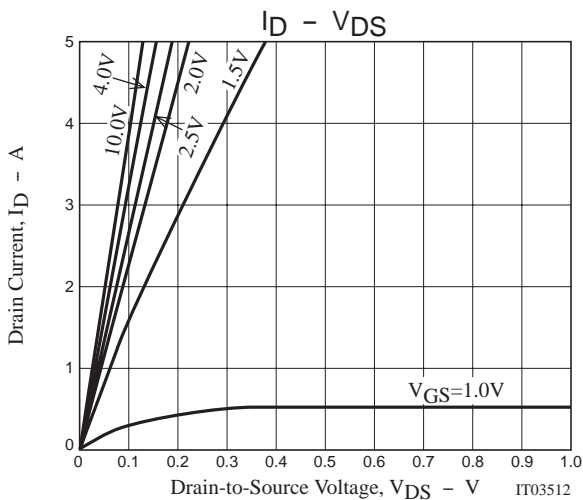
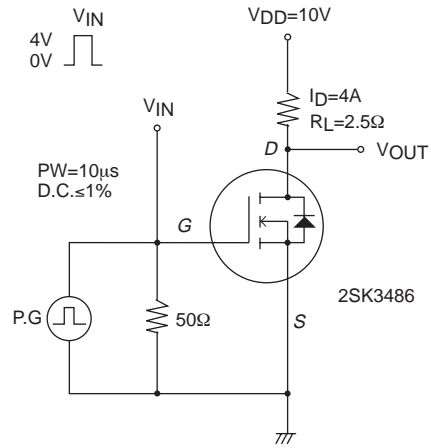
## Package Dimensions

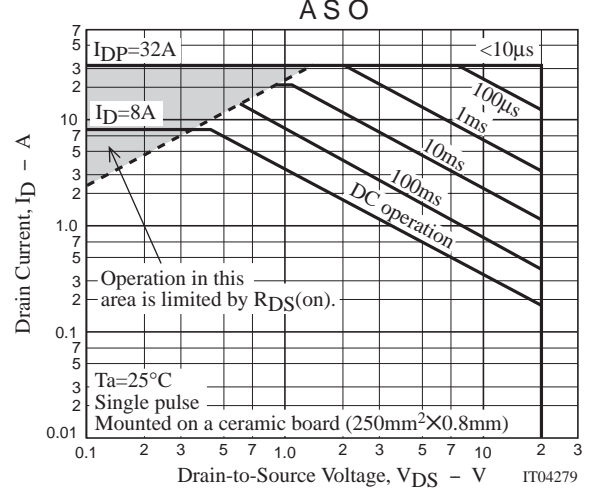
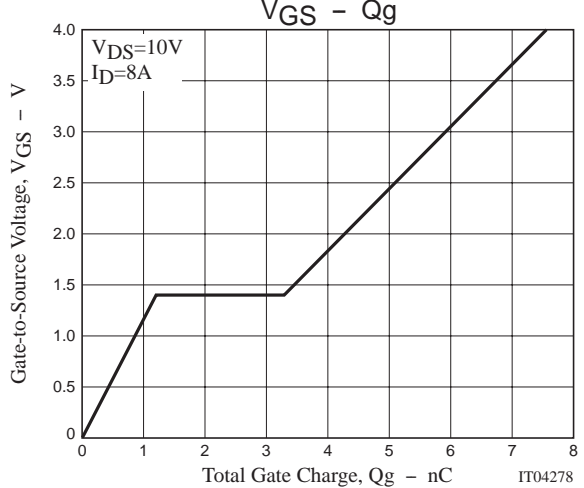
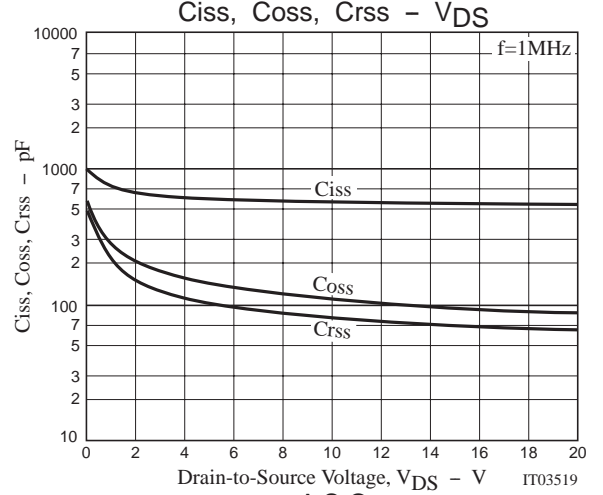
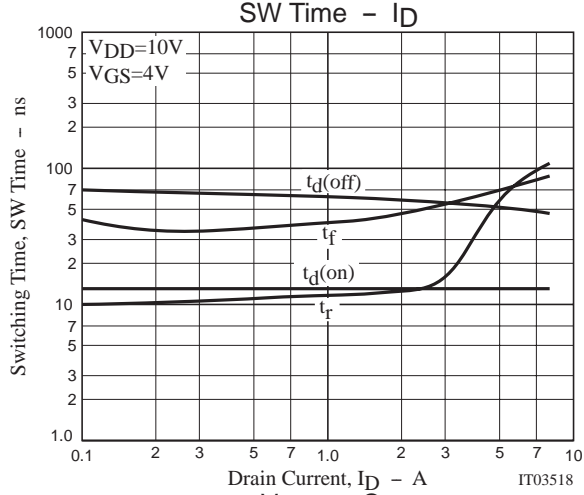
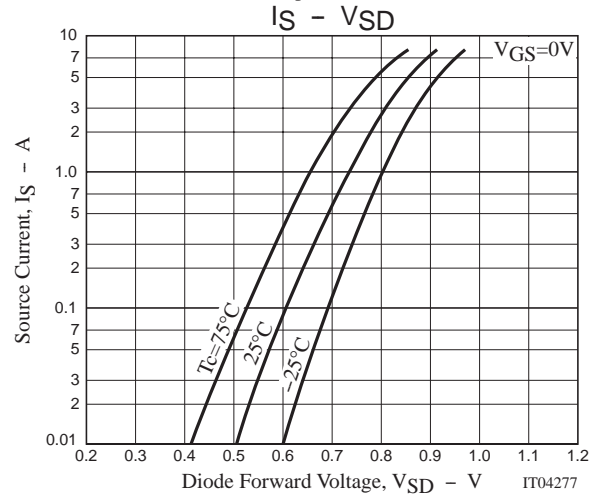
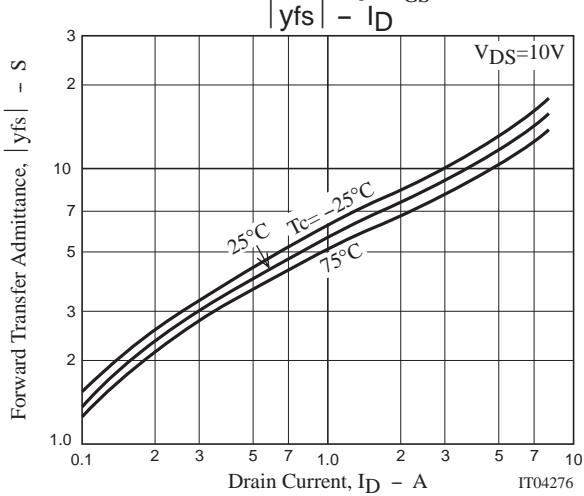
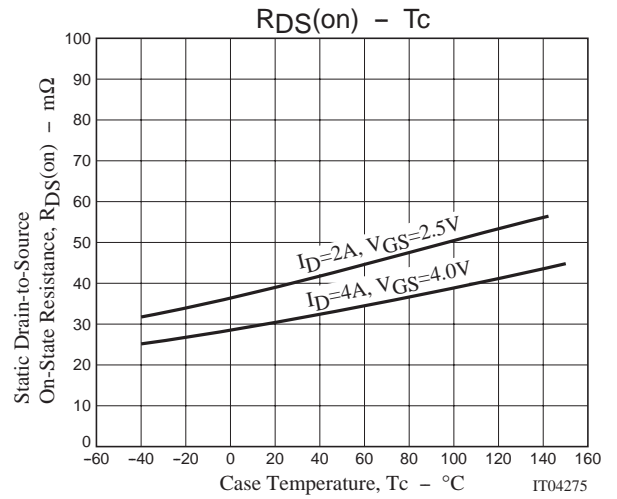
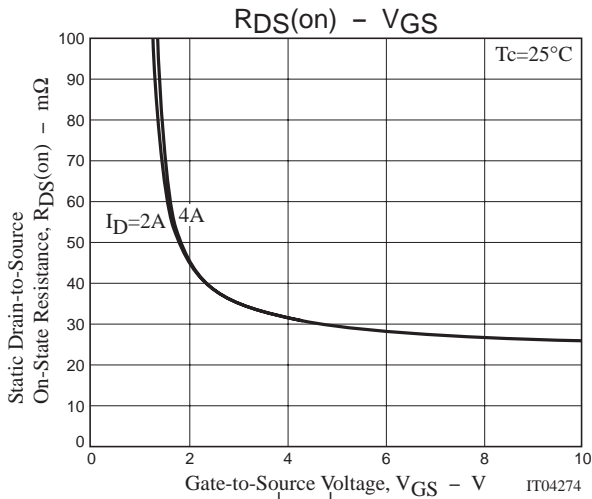
unit : mm (typ)

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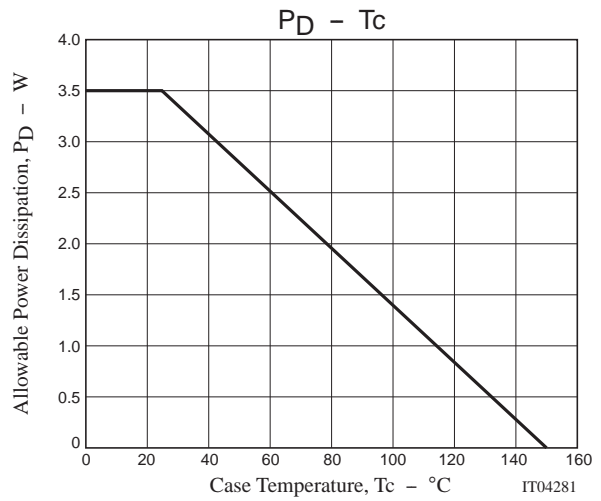
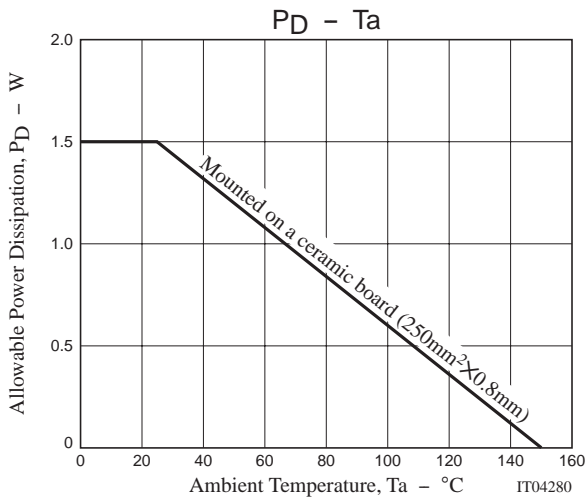


## Switching Time Test Circuit





## 2SK3486



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