SKM 75GD123D



SEMITRANSTM 3

Trench IGBT Modules

SKM 75GD123DL SKM 75GD123D SKM 75GDL123D

Features

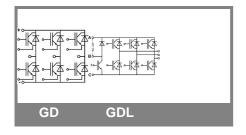
- MOS input (voltage controlled)
- N channel, homogeneous Si
- Low inductance case
- Very low tail current with low temperature dependence
- High short circuit capability, selt limiting to 6 x I_{cnom}
- · Latch-up free
- Fast & soft inverse Cal diodes
- Isolated copper baseplate using DCB Direct Bonding Technology
- Large clearance (9 mm) and creepage distance (13 mm)

Typical Applications

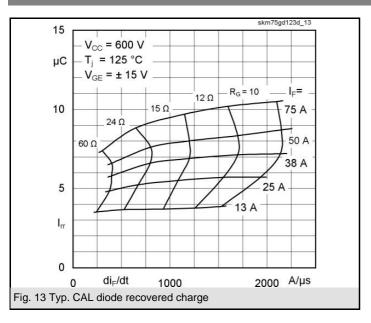
- Switched mode power supplies
- DC servo and robot drives
- Three phase inverters for AC motor speed control
- Switching (not for linear use)

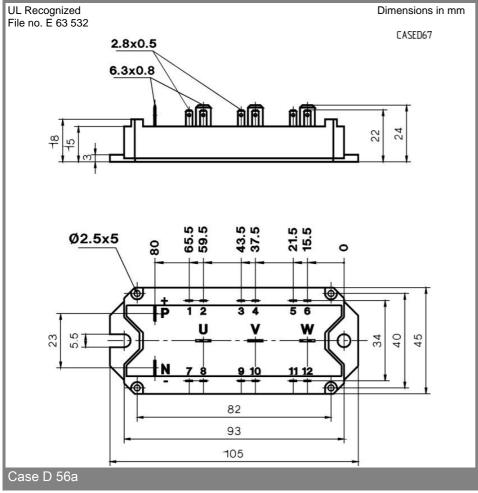
Absolute	Maximum Ratings	T _c = 25 °C, unless otherwise specified						
Symbol	Conditions	Values	Units					
IGBT								
V_{CES}		1200	V					
V _{CES} I _C	T _c = 25 (80) °C	75 (50)	Α					
I _{CRM}	t _p = 1 ms	100	Α					
V_{GES}		± 20	V					
T_{vj} , (T_{stg})	$T_{OPERATION} \leq T_{stg}$	- 40 + 150 (125)	°C					
V _{isol}	AC, 1 min.	2500	V					
Inverse diode								
I _F	T _c = 25 (80) °C	75 (50)	Α					
I _{FRM}	$t_p = 1 \text{ ms}$	100	Α					
I _{FSM}	$t_p = 10 \text{ ms; sin.; } T_j = 150 \text{ °C}$	550	Α					

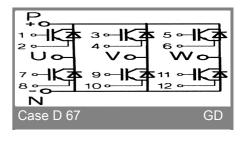
Characte	ristics T	c = 25 °C, unless otherwise specified			
Symbol	Conditions	min.	typ.	max.	Units
IGBT					
$V_{GE(th)}$	$V_{GE} = V_{CE}$, $I_C = 2 \text{ mA}$	4,5	5,5	6,5	V
I _{CES}	$V_{GE} = 0, V_{CE} = V_{CES}, T_j = 25 (125) °C$		0,4	1,2	mA
$V_{CE(TO)}$	T _j = 25 (125) °C		1,4 (1,6)	1,6 (1,8)	V
r_{CE}	V _{GE} = 15 V, T _j = 25 (125) °C		22 (30)	28 (38)	mΩ
V _{CE(sat)}	I_{Cnom} = 50 A, V_{GE} = 15 V, chip level		2,5 (3,1)	3 (3,7)	V
C _{ies}	under following conditions		3,3	4,3	nF
C _{oes}	$V_{GE} = 0$, $V_{CE} = 25 \text{ V}$, $f = 1 \text{ MHz}$		0,5	0,6	nF
C _{res}			0,22	0,3	nF
L _{CE}				60	nH
R _{CC'+EE'}	res., terminal-chip T _c = 25 (125) °C				mΩ
t _{d(on)}	V _{CC} = 600 V, I _{Cnom} = 50 A		44	100	ns
t _r	$R_{Gon} = R_{Goff} = 22 \Omega, T_j = 125 °C$		56	100	ns
$t_{d(off)}$	V _{GE} = ± 15 V		380	500	ns
t _f			70	100	ns
$E_{on} \left(E_{off} \right)$			8 (5)		mJ
Inverse d	iode				
$V_F = V_{EC}$	I_{Fnom} = 50 A; V_{GE} = 0 V; T_i = 25 (125) °C		2 (1,8)	2,5	V
$V_{(TO)}$	T _j = 25 (125) °C		1,1	1,2	V
r_T	T _j = 25 (125) °C		18	22	mΩ
I _{RRM}	$I_{Fnom} = 50 \text{ A}; T_j = 25 (125) ^{\circ}\text{C}$		23 (35)		Α
Q_{rr}	di/dt = 800 A/μs		2,3 (7)		μC
E _{rr}	V _{GE} = V				mJ
Thermal of	characteristics				
$R_{th(j-c)}$	per IGBT			0,32	K/W
R _{th(j-c)D}	per Inverse Diode			0,6	K/W
R _{th(c-s)}	per module			0,05	K/W
Mechanic	al data				
M_s	to heatsink M5				Nm
M_t	to terminals	4		5	Nm
W				175	g

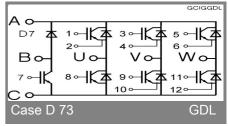


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This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, Chapter IX.

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